

**ABSTRACT**

This invention relates to an image processing device, which is applied preferably to, for example, a motion vector detection processing or the like. Each of the operation processing unit 102<sub>n</sub> receives a process 5 packet output from the process generation section 101 and performs any processing according to an instruction contained in the process packet. The units 102<sub>1</sub>-102<sub>12</sub> are divided into three suites 102a-102c and route selection sections 104<sub>1</sub>-104<sub>3</sub>, are respectively inserted to input side of each of the suites. If the unit which executes a process related to an 10 input process packet is not included in the immediately following one of the suites 102a-102c, the respective route selection sections 104<sub>1</sub>-104<sub>3</sub> supply this corresponding input process packet not to the input side of that one of the suites 102a-102c but to the output side of that suite. The 15 process packet moves as bypassing such a suite as not to have the unit that executes a process related to this process data, thereby reducing its processing time and its power dissipation.